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APPLICATION INFORMATION

Title Line One:: Subranging Analog to Digital Converter w
Title Line Two:: ith Multi-Phase Clock Timing
Total Drawing Sheets:: 9
Formal Drawings?:: No
Application Type:: Utility
Docket Number:: 1875.2820002
Secrecy Order in Parent Appl.?:: No

CONTINUITY INFORMATION

This application is a:: CONTINUATION OF
> Application One:: 10/359,201
Filing Date:: 02-06-2003

Which is a::CONTINUATION OF
>> Application Two:: 10/158,773
Filing Date:: 05-31-2002

Which is a::CONTINUATION IN PART OF
>>> Application Three:: 10/153,709
Filing Date:: 05-24-2002

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